

DESIGN ENVIRONMENT FOR EXTREME-SCALE BIG DATA ANALYTICS ON HETEROGENEOUS PLATFORMS

DATE22 Workshop

Climbing EVEREST: dEsign enVironmEnt foR Extreme-Scale big data analyTics on heterogeneous platforms

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EVEREST: Big Data Processing on FPGA

H2020 project funded under the call – "Big Data technologies and extreme-scale analytics" [Kick-off on Oct 1, 2020][http://www.everest-h2020.eu]

• Big focus on <u>FPGA acceleration in data centers and associated issues</u>

Key idea:

- a coordinated action with the appropriate technology areas (e.g., AI, analytics, software engineering, HPC, Cloud technologies) → FPGA acceleration in (federated) data centers
- system engineering/tools to contribute to the co-design of federated/distributed systems → EVEREST system development kit

adaptive memory management			architectures for collecting,
domain-specific extensions	data security		managing and exploiting data
high-level synthesis hardware acceleration			
runtime management	virtualization	autotuning and dunamic adaptation	



EVEREST Partners

IBM.

IBM Reseach Lab, Zurich (Switzerland) Project coordination, prototype of the target system



Politecnico di Milano (Italy) Scientific management, high-level synthesis, flexible memory managers, dynamic autotuning



Università della Svizzera italiana (Switzerland) Data security requirements and protection techniques



TU Dresden (Germany) Domain-specific extensions, code optimizations and variants generation



Virtual Open Systems (France) Virtualization techniques, runtime extensions to manage heterogeneous resources



IT4Innovations (Czech Republic) Exploitation leaders, HPC infrastructure, workflow libraries



Centro Internazionale di Monitoraggio Ambientale (Italy) Weather prediction models



Duferco Energia (Italy) Application for prediction of renewable energies



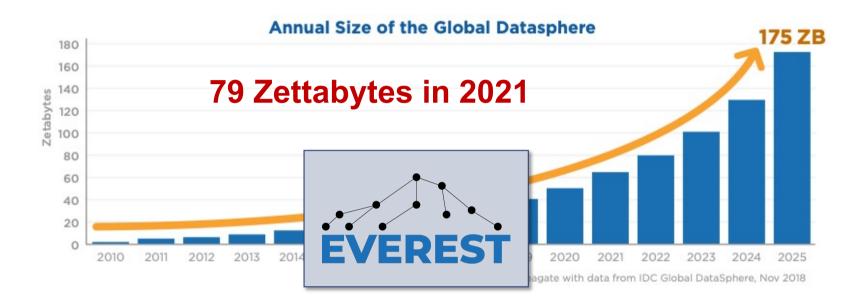
Numtech (France) Application for monitoring the air quality of industrial sites



Sygic A/S (Slovakia) Application for intelligent transportation in smart cities



Data Driven World











Outline

- Everest Use Cases
- Everest Target Architectures
- Everest System Development Kit

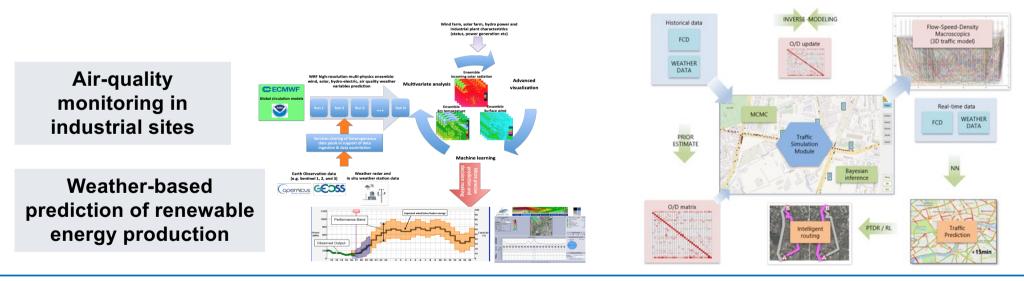


Everest Use Cases: Application Concepts

Three use cases provided by the application partners

- Looking for hardware acceleration (intense data computation) with efficient and secure data management (distributed data sources)
- Possibility of AI/ML-based decision making
- Combination of tasks in different pipelines (creation of "scientific services")

Traffic modelling for intelligent transportation

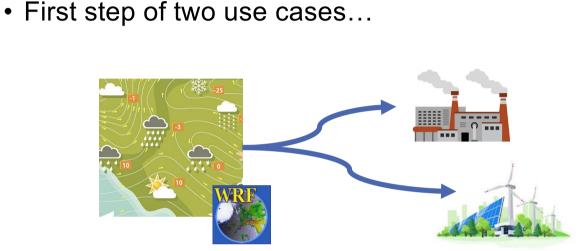




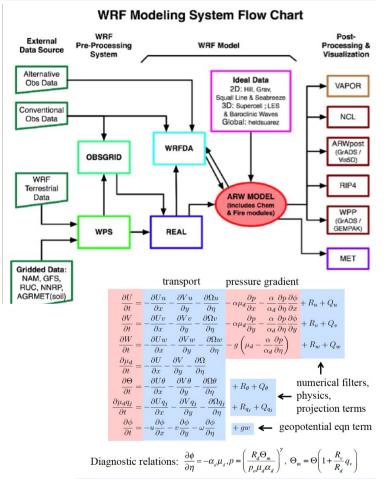
The WRF Model

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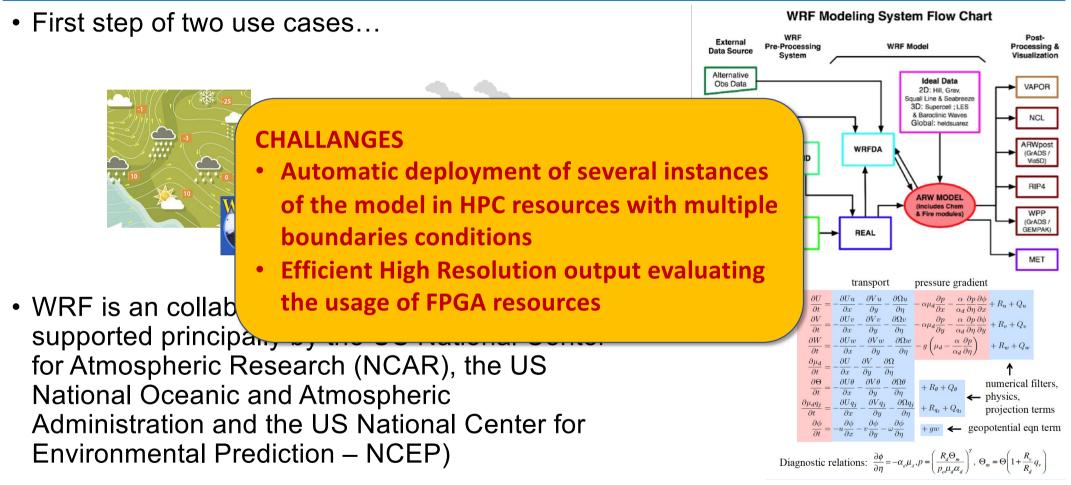
 WRF is an collaborative open source model supported principally by the US National Center for Atmospheric Research (NCAR), the US National Oceanic and Atmospheric Administration and the US National Center for Environmental Prediction – NCEP)





The WRF Model

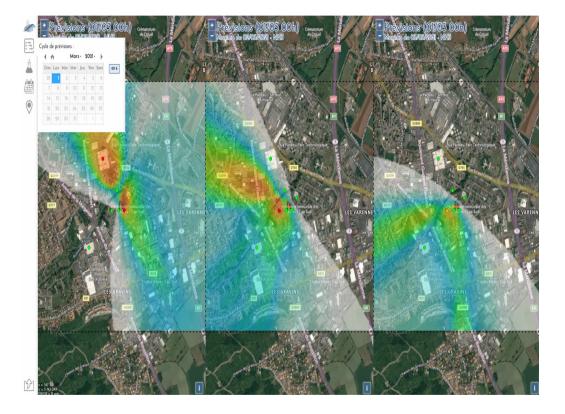






Air-quality use case: General context





- Platform to predict the air quality of industrial sites
- If predicted impacts exceeds some concentration levels, the industrial site may adapt its production, activate some emission reduction process, ...
 - Action = Financial cost
 - Objectives :
 - Minimize false predicted peaks
 - Optimize real predicted peaks
- One key part is the meteorological forecast part
- Site specific predictions



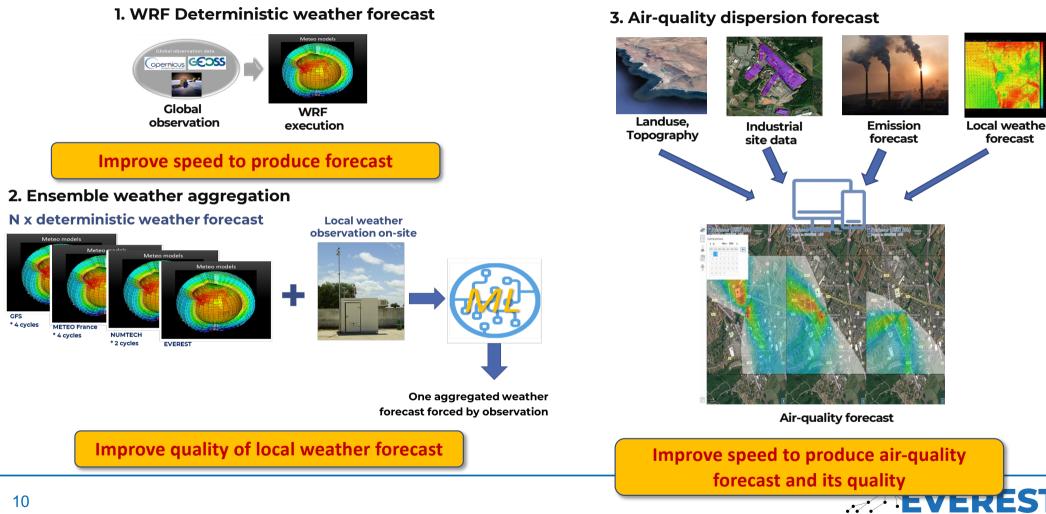
Air-quality use case: Workflows and Challenges



Local weather

forecast

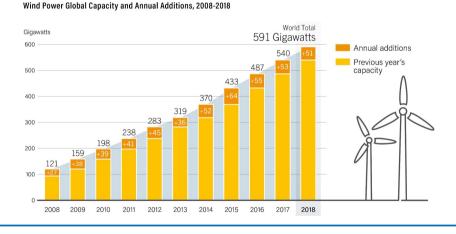
forecast



Renewable Energy use case: Context and Challenge

- Effort to improve the Wind Power Forecast accuracy
- Different challenges due to intermittency of the wind power generation:
 - <u>Transmission System Operator</u> (in Italy TERNA) to ensure the balance of grid (very short term horizon: 1s to 1h)
 - <u>Traders</u> to forecast the power to sell on energy market, intraday or day ahead (short term horizon: 1h to 24h)
 - <u>Wind farm owners</u> to schedule their maintenance programs (long term horizon)

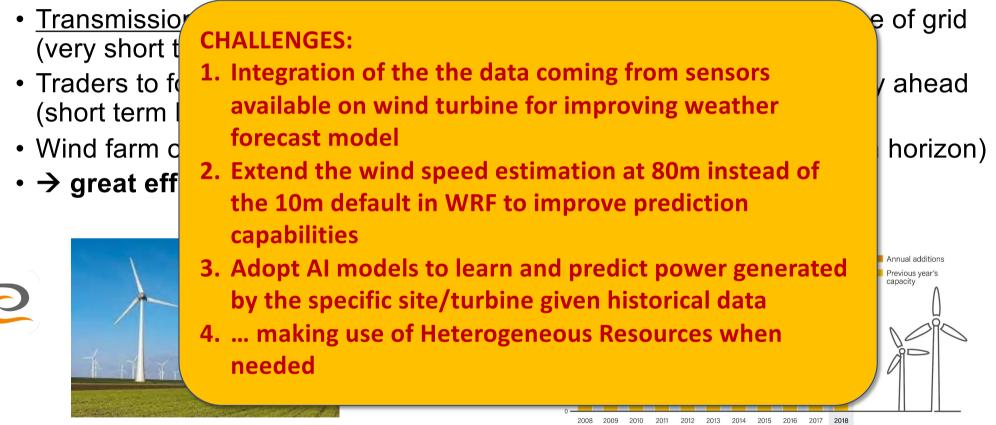






Renewable Energy use case: Context and Challenge

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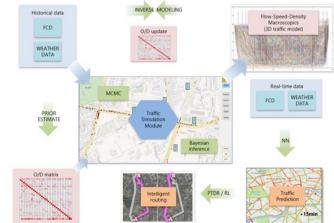




Advance Traffic Modeling for Smart Cities use case

- Mobility platform for supporting cities with advanced traffic modelling
- Data sources
 - Historical and real-time Floating Car Data (FCD)
 - e.g. GPS position, timestamp and speed
 - Origin-destination matrix (ODM) defining city
 - Road network graph including road restrictions;
 - Historical weather data (temperature, precipitation)
- Traffic services
 - What-if analysis for given scenarios, e.g. road closure;
 - Intelligent routing for large amount of vehicles
 - Traffic prediction for major road elements of cities







Advance Traffic Modeling for Smart Cities use case

 Mobility platform for supporting cities with advanced traffic modelling Data source **CHALLENGES:** Historical a 1. Boost the FCD data collection and processing to • e.g. GPS compute 3D traffic models Origin-des 2. Develop and efficiently deploy a traffic simulator on a Road netw multinode architecture to predict traffic behaviour Macroscop (3D traffic m Historical w **3. Adopt AI techniques to learn the traffic patterns** resulting into a traffic prediction service. Traffic servi 4. ... making use of Heterogeneous Resources What-if and Intelligent routing for large amount of vehicles • Traffic prediction for major road elements of cities

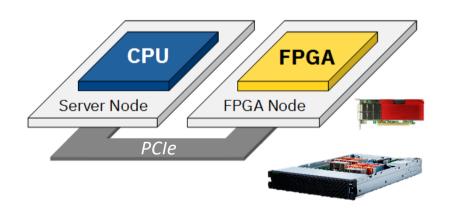


EVEREST Target System: Brief Overview

Datacenters and **Supercomputer** infrastructures

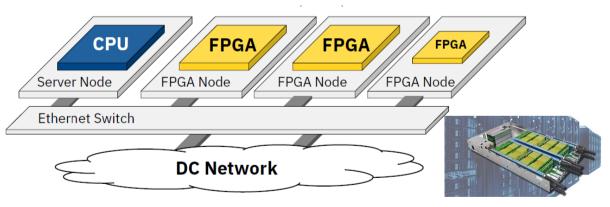
Network-attached and **PCIe-attached** FPGSd architectures:

- Off-the-shelf FPGA devices
- User logic can be easily designed and customized with traditional HLS tools



FPGA as a Co-Processor

FPGA as a Peer-Processor







cloudFPGA



UltraScale XCKU060





- FPGA disaggregated from the server nodes
- FPGAs connected directly to the DC network for its access and to communicate with CPUs and other FPGAs
- FPGAs are den densely packed into DC chassis and racks and distributed across the DC

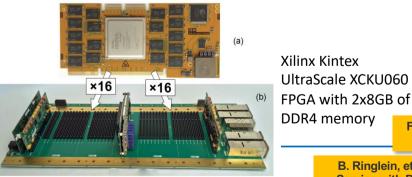
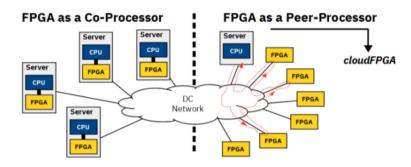
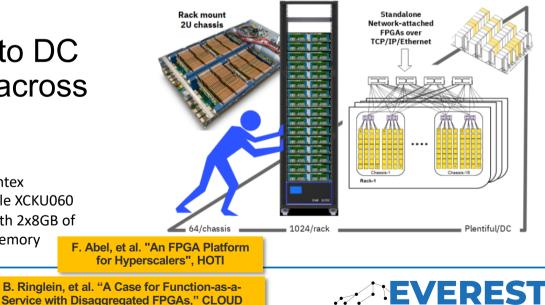


Figure 1: (a) The disaggregated FPGA and (b) the carrier board.

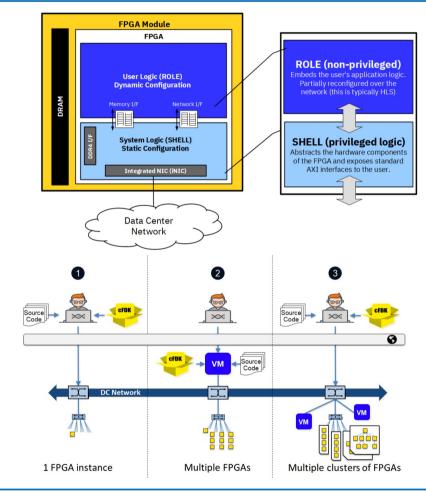




cloudFPGA: Key Features

- Network-attached solution composed of:
 - Interface logic already designed (cF Shell) to support system integration
 - TPC/UDP communication is managed transparently to the user logic
 - User logic (ROLE) that can be easily designed and customized with traditional HLS tools
- Application code running on host
 - FPGA accessible through the network
 - Low-level libraries for host-FPGA communication
- Possibility to instanciate a cluster of FPGAs
- IDE and custom tools for allocation and management of resources







Climbing EVEREST: Obstacles on the Road

Programmability: Application developers have often limited hardware skills and limited knowledge of the target platform

- How to specify the application functionality to get the best results?
- How to design the hardware accelerator and the memory subsystem not only to optimize the performance but also to respect resource constraints

Portability: Designing a FPGA system is hard, but **designing an application for many systems** is even harder

- How to specify a platform-agnostic functionality?
- How to match such functionality with the actual hardware?
- How to deal with dynamic changes?



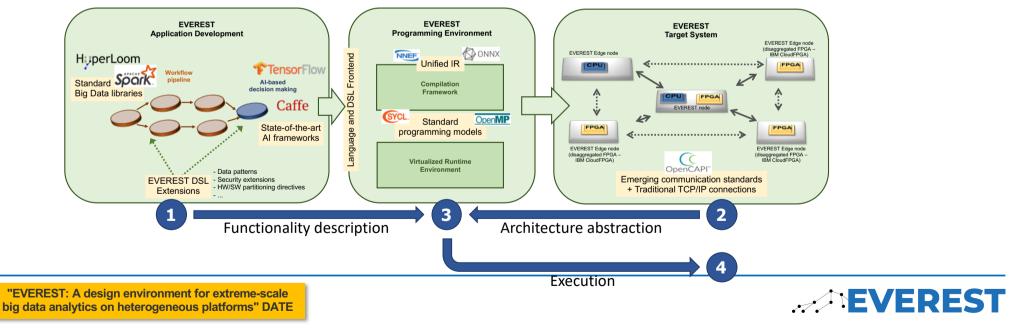


EVEREST SDK: <u>SYSTEM</u> Development Kit

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Coordinated design environment composed of four major phases:

- **1. Application specification** (data ▷ application and requirements)
- **2.** Architecture abstraction (target system ▷ arch. description)
- 3. Programming environment (app+arch+reqs desc. ▷ hw/sw bin.)
- **4. Execution monitoring and management** (hw/sw bin. ▷ execution)

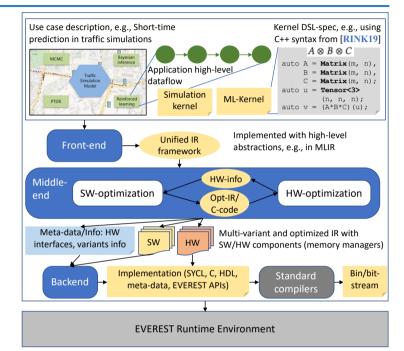


EVEREST Programming Environment

Compilation Environment: analyzes application and creates all "variants" based on <u>architecture abstraction</u> and <u>application/data</u> <u>requirements</u>

- Integration of functional description and nonfunctional properties with DSL extensions
- Unified IR framework (MLIR) to support different inputs
 - Support for custom DSL and existing ML frameworks
- Interoperability of HLS tools (Bambu, VivadoVitis HLS)
- "Intelligent" **memory managers** to coordinate data transfers (Mnemosyne)
- Multitarget system integration for different FPGAs (Olympus, cFDK)

F. Ferrandi et al., "Bambu: an Open-Source Research Framework for the High-Level Synthesis of Complex Applications" DAC C. Pilato et al. "System-Level Optimization of Accelerator Local Memory for Heterogeneous Systems-on-Chip" TCAD



Creation of bitstreams and binaries to be deployed onto the target platform hiding its complexity

C. Friebel et al. "From Domain-Specific Languages to Memory-Optimized Accelerators for Fluid Dynamics." CLUSTER

EVEREST Runtime Environment

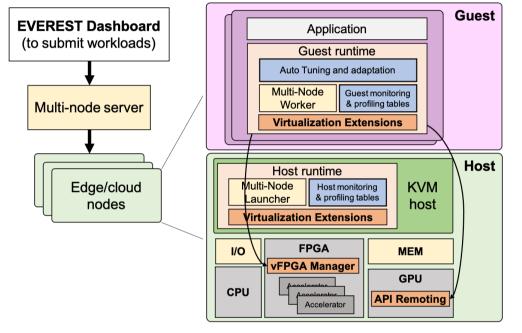
Runtime Environment: implements the <u>dynamic selection of "variants"</u> and the <u>hardware configuration</u> based on the <u>system status</u>

• Two-level runtime to support

(1) **virtualization** of hardware resources regardless their distribution and the low-level details of the platforms;

(2) Application-level functional decisions

- (a) mARGOt for dynamic adaptation and autotuning
- (b) HyperLoom/Hyperqueue for multinode management (scheduling and resource allocation)



Automatic deployment and configuration on the (cloud) FPGA resources

D. Gadioli, et al. "mARGOt: A Dynamic Autotuning Framework for Self-Aware Approximate Computing" TCOM V. Cima et al."HyperLoom: A platform for defining and executing scientific pipelines in distributed environments." PARMA-DITAM



Conclusions

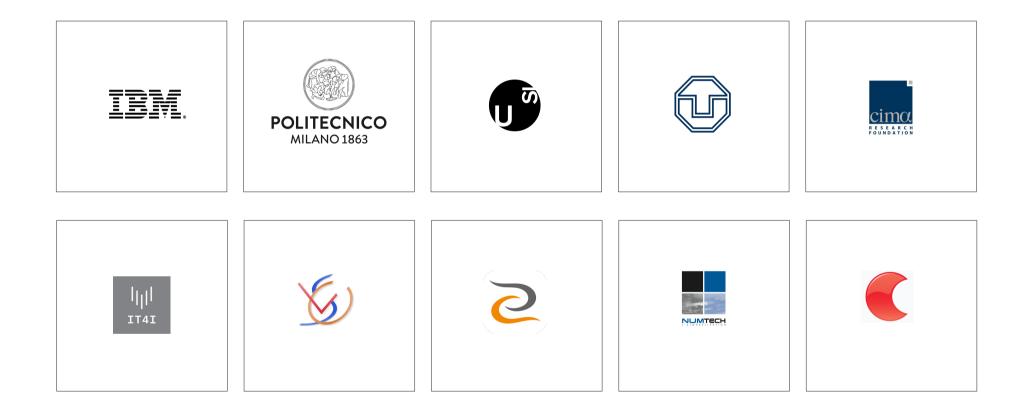
EVEREST is an H2020 project that aims at **simplyfing the use of FPGA** for the acceleration of Big Data applications

- Three application use cases: weather-based renewable energy prediction, airquality monitoring of industrial sites, and intelligent traffic management
- Data-centric approach focusing on the combination of domain-specific and dataoriented extensions, high-level synthesis, and dynamic adaptivity
- CloudFPGA and Alveo-based clusters will be used as acceleration platforms
- Looking for interoperability with existing solutions
- EVEREST SDK will be released as open-source to the community

The main goal is to **simplify the description of complex Big Data applications** and **improve the programmability of distributed FPGA-based systems**

!!! Many piecies available, the global picture is not composed yet !!! Stay tuned on <u>http://www.everest-h2020.eu</u>







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